

Monday 2nd November

08:00 Registration desk open

09:00 Invited Talk (Chair: XXX)

Ken McMillan
Cadence, USA
What's in Common Between Test, Model Checking, and Decision Procedures?

10:00 - 10:30 Coffee break

10:30 Session 1 (Chair: XXX)

David Delmas, Eric Goubault, Sylvie Putot, Jean Souyris, Karim Tekkal and Franck Védrine
Towards an Industrial Use of FLUCTUAT on Safety-Critical Avionics Software

Alwyn Goodloe and Cesar Muñoz
Compositional Verification of a Communication Protocol for a Remotely Operated Vehicle

11:30 Poster Session 1 (Chair: XXX)

Alessio Ferrari, Alessandro Fantechi, Stefano Bacherini and Niccolò Zingoni
Formal Development for Railway Signaling Using Commercial Tools

Nassima Izerrouken, Marc Pantel, Xavier Thirioux and Olivier Ssi Yan Kai
Integrated Formal Approach for Qualified Critical Embedded Code Generator

Laura Panizo, María del Mar Gallardo, Pedro Merino and Antonio Linares
Developing a Decision Support Tool for Dam Management with SPIN

12:30 - 14:30 Lunch

Monday 2nd November

14:30 Invited Talk (Chair: XXX)

Dino Distefano
Queen Mary, University of London, UK
Attacking Large Industrial Code with Bi-Abductive Inference

15:30 - 16:30 Coffee break

16:30 Session 2 (Chair: XXX)

José Bacelar Almeida, Manuel Barbosa, Jorge Sousa Pinto and Bárbara Vieira
Correctness with Respect to Reference Implementations

Erik Schierboom, Alejandro Tamalet, Hendrik Tews, Marko van Eekelen and Sjaak Smetsers
Preemption Abstraction: a Lightweight Approach to Modelling Concurrency.

Javier de Dios and Ricardo Peña
Certified Implementation on top of the Java Virtual Machine

19:00 Social dinner + EASST award to best paper

Tuesday 3rd November

08:00 Registration desk open

09:00 Invited Talk (Chair: XXX)

Diego Latella
ISTI/CNR, Italy
On a Uniform Framework for the Definition of Stochastic Process Languages

10:00 - 10:30 Coffee break

10:30 Session 3 (Chair: XXX)

Matthias Raffelsieper, MohammadReza Mousavi, Jan-Willem Roorda, Chris Strolenberg and Hans Zantema
Formal Analysis of Non-Determinism in Verilog Cell Library Simulation Models

Julio Mariño, Ángel Herranz, Manuel Carro and Juan José Moreno-Navarro
Formal Modeling of Concurrent Systems with Shared Resource

Kenneth J. Turner and Koon Leai Larry Tan
A Rigorous Methodology for Composing Services

12:30 - 14:30 Lunch

Tuesday 3rd November

14:30 Invited Talk (Chair: XXX)

Thierry Lecomte
ClearSy, France

Applying a Formal Method in Industry: a 15-year trajectory

15:30 Poster Session 2 (Chair: XXX)

Lukas Ladenberger, Jens Bendisposto and Michael Leuschel

Visualizing Event-B models with BMotionStudio

Wojciech Mostowski, Erik Poll, Julien Schmaltz, Jan Tretmans and Ronny Wichers Schreur

Model-Based Testing of Electronic Passports

Aad Mathijssen, Yaroslav S. Usenko and Dragan Bosnacki

Behavioural Analysis of an I2C Linux Driver

16:00 - 16:30 Coffee break

16:30 Session 4 (Chair: XXX)

Pavel Parizek and Tomas Kalibera

Platform-Specific Restrictions on Concurrency in Model Checking of Java Programs

Sami Evangelista and Lars Michael Kristensen

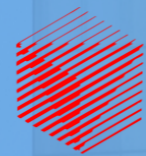
Dynamic State Space Partitioning for External Memory Model Checking

17:30 FMICS 2009 closure and FMICS WG meeting

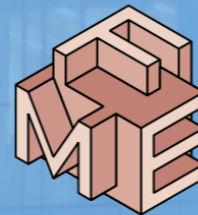
TU/e



UNIVERSIDAD
POLITECNICA
DE VALENCIA



ERCIM



Microsoft

Research

DSIC



TU/e



Springer

14th International Workshop on Formal Methods for Industrial Critical Systems (FMICS 2009)

Program Chairs

María Alpuente

Technical University of Valencia, Spain

Byron Cook

Microsoft Research, UK

Workshop Chair

Christophe Joubert

Technical University of Valencia, Spain



Eindhoven, The Netherlands.
2-3 November 2009

<http://www.dsic.upv.es/workshops/fmics2009>